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1. A multiple die package comprising:

a pair of dies having bonding pads and front

surfaces on which said bonding pads are located, said front

surfaces facing oppositely from one another; and

a leadframe, at least one of said dies secured

on said leadframe and with a bonding pad of one of said dies

electrically connected to said leadframe.

- 2. The package of claim 1, wherein said dies are stacked one on top of the other.
- 1 3. The package of claim 2, wherein said leadframe 2 is secured to one of said dies.
  - 4. The package of claim 2, wherein each of said dies includes a back surface opposite to said front surface, said back surface of each of side dies connected to one another.
  - 5. The package of claim 2, wherein each of said dies includes a back surface opposite to said front surface, said back surface of each die facing one another, each of said back surfaces connected to said leadframe.
- 1 6. The package of claim 2, wherein each of said 2 dies are secured to said leadframe.
- 7. The package of claim 6, wherein each of said dies are secured to said leadframe at a different location.
- 1 8. The package of claim 6, wherein leadframe 2 includes an offset.

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The package of claim 8, wherein each of said 1 9. dies is connected to the same side of said leadframe. The package of claim 8, wherein each of said 1 dies is connected to a different side of said leadframe. 2

A multiple die package comprising:

a pair of dies having bonding pads and front

surfaces on which said bonding pads are located, said front

surfaces facing in the same direction;

a leadframe, at least one of said dies secured

to said leadframe; and

a spacer for spacing said dies from one

another.

- The package of claim 11, wherein said spacer is secured to said leadframe and one of said dies is secured to said spacer.
- The package of claim 11, wherein said spacer is integral with said leadframe.
  - The package of claim 13, wherein said spacer is formed by an offset portion of said leadframe.
- A method for mounting multiple semiconductor dies on a single leadframe, comprising:

stacking at least two semiconductor dies having substantially the same rectangular dimensions on top of one

another; and

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electrically connecting the semiconductor dies

7 to the leadframe.

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1	16. The method of claim 15, wherein a first
2	semiconductor die is mounted back to back on a second
3	semiconductor die.
1	17. The method of claim 16, wherein the first
2	semiconductor die is adhered to the second semiconductor die
3	by an adhesive layer.
1	18. The method of claim 15, wherein a first
2	semiconductor die has a lead-on-chip configuration.
1	19. The method of claim 15, wherein one of said
2	dies is secured to said leadframe and the other of said dies
3	is secured to the die secured to the leadframe.
1	20. The method of claim 15, further comprising
2	wirebonding the semiconductor dies to the leadframe, said
3	dies having facing sides and outwardly facing sides by
4	extending wires to bond pads on the outwardly facing sides
5	of said die.
	•
1	21. A method of connecting multiple semiconductor
2	dies having bonding pads and a single leadframe having lead
<b>4</b>	fingers, comprising:
- 1	locating a first semiconductor die on the lead
Į F	fingers of the leadframe)
Γ.	stacking accord somiconductor die on said

semiconductor dies to the lead fingers of the leadframe.

electrically connecting the bonding pads of the

first semiconductor die; and

	T	22. The method of Claim 21, further comprising
	2	encapsulating the semiconductor dies and the leadframe in a
	3	single package body.
the after a limit to the first time the first time that the first time the first time time time time time time time tim	1	23. A semiconductor device, comprising:
	2	a plurality of semiconductor dies having about
	3	the same rectangular dimensions;
	4	a leadframe having lead fingers to which the
	5	semiconductor dies are mounted; and
	6	connectors for electrically connecting the die
	7	to the leadframe.
	1	24. The semiconductor device of claim 23, further
	2	comprising:
	3	a first semiconductor die mounted to a first
	4	side of the leadframe; and
	5	a second semiconductor die mounted to a second
	6	different side of the leadframe.
<b>=</b>	1	25. The semiconductor device of claim 23, wherein
	2	the lead fingers have a first portion and a second portion,
	3	further comprising:
	4	a first semiconductor die mounted on a first
		portion of the lead fingers; and
	6	a second semiconductor die mounted on a second
	7	different portion of the lead fingers, wherein the second
	8	portion is offset from the first portion.

The semiconductor device of claim 23, further 26. 1 2 comprising: 3 a support member mounted to a first side of the 4 leadframe; a first semiconductor die mounted onto the 5 6 support frame; and 7 a second semiconductor die mounted to a second, different side of the leadframe. 8 A package for multiple semiconductor dies, 1 2 comprising: a plurality of semiconductor dies stacked one 3 4 on top of the other; □ 5 a leadframe having lead fingers on which the ű semiconductor dies are mounted; and 6 Q a package body for hermetically encapsulating **II** 7 Ш the semiconductor dies and the leadframe. 8 4 <u>\_</u> **⊭** 1 28. A semiconductor device, comprising: a leadframe having a first surface, a second 2 **U** 3 surface opposite said first surface, and lead fingers; a first die located on the first surface, the first die having bond pads which are electrically contacted to the lead fingers on the first surface of the leadframe; 6 and 7 a second die located on the second surface, the 8 second die having bond pads which are electrically contacted 9 to the lead fingers on the second surface of the leadframe. 10

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conductive material.

1	29. An integrated circuit package, comprising:
2	a leadframe having first and second surfaces;
3	a support member mounted to a first surface of
4	the leadframe;
5	a first die mounted to the support member; and
6	a second dre mounted to a second surface of the
7	leadframe.
1	30. The integrated circuit package of claim 29,
2	wherein the support member is made of an electrically non-

31. The integrated circuit package of claim 29, wherein each die has bond pads mounted on and a surface on which said bond pads are mounted, said surfaces of each die facing in the same direction.

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